IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application:
Application No.:
Filed:
Title:
Commissioner for patents
Washington, D.C.   20231

POWER OF ATTORNEY BY ASSIGNEE OF ENTIRE INTEREST
(REVOCATION OF PRIOR POWERS)
As assignee of record of each of the patent applications listed in the table of attachment A,

REVOCATION OF PRIOR POWERS OF ATTORNEY
all powers of attorney previously given in each of the listed patent applications are hereby revoked, and

NEW POWER OF ATTORNEY
with full power of revocation

the following attorneys/agents are hereby appointed to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith: I hereby appoint all attorneys of Thomas, Kayden, Horstemeyer & Risley, LLP, who are listed under the USPTO Customer Number shown below as the attorneys to prosecute this application and to transact all business in the United States Patent and Trademark Office connected therewith, recognizing that the specific attorneys listed under that Customer Number may be changed from time to time at the sole discretion of Thomas, Kayden, Horstemeyer & Risley, LLP, and request that all correspondence about the application be addressed to the address filed under the same USPTO Customer Number.

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Patent Trademark Office

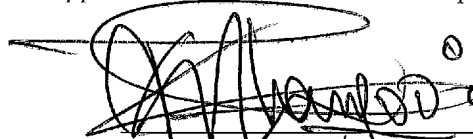Please direct all future correspondence and telephone calls to:
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ASSIGNEE CERTIFICATION

The certification under 37 C.F.R. §3.73(b) establishing the right of assignee to take action is attached hereto along with documentation evidencing same. Further, in my official position with Taiwan Semiconductor Manufacturing Company, Ltd., I am authorized to sign documents and otherwise act on its behalf in connection with the management and handling of patent applications and other intellectual property matters.

Date: 10/3/2008

Keith A. Chanroo
Director - Intellectual Property Division

0503-A33537H-US

Attachment A

No.	Serial No	Patent No.	Application Title	Filing Date	Assignment (Reel/Frame)
1.	08/976,683	6,018,777	Intelligent SCSI-2/DMA processor	11/24/1997	1. 009089/0936 2. 009596/0539 3. 015246/0599 4. 015279/0556 5. 016216/0649
2.	09/100,448	6,430,589	Single precision array processor	06/19/1998	1. 009269/0067 2. 015246/0771 3. 015232/0800 4. 016216/0649
3.	10/167,004	6,721,773	Single precision array processor	06/10/2002	1. 015246/0771 2. 015232/0800 3. 016216/0649
4.	08/783,782	6,289,065	FIFO status indicator	01/15/1997	1. 009089/0936 2. 009596/0539 3. 015246/0599 4. 015279/0556 5. 016216/0649
5.	08/725,913	RE36874	Supply voltage tolerant phase-locked loop circuit	10/04/1996	1. 009089/0936 2. 009596/0539 3. 015246/0599 4. 015279/0556
6.	10/719,357	7,207,025	Sea-of-cells array of transistors	11/21/2003	018948/0842
7.	10/463,160	7,257,779	Sea-of-cells array of transistors	06/16/2003	1. 015246/0599 2. 015279/0556 3. 016216/0649
8.	09/388,333	6,039,057	Automated washing system and method	09/02/1999	1. 015246/0599 2. 015279/0556 3. 016216/0649
9.	09/322,967	6,225,154	Bonding of silicon wafers	05/28/1999	1. 015246/0599 2. 015279/0556 3. 016216/0649
10.	08/739,394	6,134,631	Non-volatile memory with embedded programmable controller	10/31/1996	1. 008287/0598 2. 015246/0771 3. 015232/0800 4. 016216/0649
11.	08/905,922	6,195,446	Digitizer stylus storing handwriting data	08/05/1997	1. 009089/0936 2. 009396/0441 3. 009596/0539 4. 015246/0599 5. 015279/0556 6. 016216/0649
12.	09/409,211	6,377,248	High voltage stylus for portable computer	09/30/1999	016216/0649

13.	08/828,246	6,515,839	Electronic discharge protection system for mixed voltage application specific circuit design	03/31/1997	1. 009089/0936 2. 009596/0539 3. 015246/0599 4. 015279/0556 5. 016216/0649
14.	08/570,256	6,240,496	Architecture and configuring method for a computer expansion board	12/11/1995	1. 009089/0936 2. 009596/0539 3. 015246/0599 4. 015279/0556 5. 016216/0649
15.	08/808,849	6,272,600	Memory request reordering in a data processing system	02/28/1997	1. 008741/0834 2. 015246/0771 3. 015232/0800 4. 016216/0649
16.	09/777,264	6,711,658	Architecture and configuring method for a computer expansion board	02/05/2001	1. 015246/0599 2. 015279/0556 3. 016216/0649
17.	09/145,824	6,449,273	Multi-port packet processor	09/02/1998	1. 009447/0795 2. 015246/0771 3. 015232/0800 4. 016216/0649
18.	08/993,442	6,854,003	Video frame rendering engine	12/18/1997	1. 009222/0725 2. 015246/0771 3. 015232/0800 4. 016216/0649
19.	09/662,068	6,976,135	Memory request reordering in a data processing system	09/14/2000	1. 011102/0563 2. 015246/0771 3. 015232/0800 4. 016216/0649
20.	09/703,948	6,675,361	Method of constructing an integrated circuit comprising an embedded macro	10/31/2000	1. 015246/0599 2. 015279/0556 3. 016216/0649
21.	10/601,351	6,967,361	Sea-of-cells array of transistors	06/23/2003	1. 015246/0599 2. 015279/0556 3. 016216/0649
22.	10/125,925	6,977,399	Sea-of-cells array of transistors	04/19/2002	1. 015246/0599 2. 015279/0556 3. 016216/0649
23.	09/703,767	6,489,641	Sea-of-cells array of transistors	11/01/2000	1. 015246/0599 2. 015279/0556 3. 016216/0649
24.	09/704,115	6,605,499	Sea-of-cells array of transistors	11/01/2000	1. 015246/0599 2. 015279/0556 3. 016216/0649
25.	08/934,239	N/A	Fast Write I/O Handling In A Disk Array Using Spare Drive For Buffering	09/19/1997	
26.	08/971,464	N/A	Method and apparatus for polymer coating substrates	11/17/1997	